

Notice of Allowability	Application No.	Applicant(s)	
	10/602,640 Examiner	SHIMAMURA, AKIMITSU Art Unit	
	William M. Treat	2181	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to the paper filed on 8/18/2006.
2. The allowed claim(s) is/are 12-17.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date 7/21/06
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

W.M.T.
WILLIAM M. TREAT
PRIMARY EXAMINER

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

In claim 12, line 9, change "extension" to "execution".

In claim 13, line 9, change "extension" to "execution".

In claim 14, line 9, change "extension" to "execution".

In claim 15, line 9, change "extension" to "execution".

In claim 16, line 9, change "extension" to "execution".

In claim 17, line 10, change "extension" to "execution".

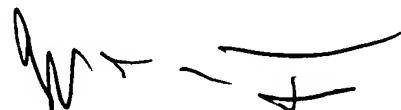
2. The following is an examiner's statement of reasons for allowance: The prior art of record does not teach a conditional branching system using the upper and lower bits of the fourth address as in claims 12 and 14 and the lower bits of the fourth address as in claims 13 and 15. As for claim 17, the prior art of record did not teach "the instruction memory comprising a memory array comprising a plurality of memory cells each specified by upper bits and lower bits of a first address, the instruction memory associating, with a second address, an instruction program having a plurality of instruction codes, the upper bits and the lower bits of the second address being designated in the memory array to perform the instruction program read out from the memory array of the instruction memory, said method comprising: comparing a conditional branch instruction address and a branch target instruction address, each address specifies an instruction program stored in a memory array of an instruction

memory, determining whether upper bits of the conditional branch instruction address and the upper bits of the branch target address differ, and re-mapping the branch target instruction address so that no carry into the upper bits is caused, thereby optimizing arrangements of the conditional branch instruction address and the branch target instruction address, when the upper bits of the conditional branch instruction address and the branch target instruction address differ."

3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

4. Any inquiry concerning this communication should be directed to William M. Treat at telephone number (571) 272-4175.

5. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



WILLIAM M. TREAT
PRIMARY EXAMINER